Inference and Declaration of Independence: Impact on Deterministic Task Parallelism

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ABSTRACT
We present a set of static techniques that reduce runtime overheads in task-parallel programs with implicit synchronization. We use a static dependence analysis to detect non-conflicting tasks and remove unnecessary runtime checks. We further reduce overheads by statically optimizing task creation and management of runtime metadata. We implemented these optimizations in SCOOP, a source-to-source compiler for such a programming model and runtime system. We evaluate SCOOP on 10 representative benchmarks and show that our approach can improve performance by 12% on average.

1. INTRODUCTION
Thread-based programming is a low-level abstraction for describing parallel computations, close to the underlying hardware model of processor cores and communication via shared memory variables. Multithreaded programs require the programmer to reason about all thread interactions and manually synchronize conflicting threads, which is difficult and error prone. Task-based parallelism offers a higher level abstraction to the programmer, making it easier to express parallel computation. Early task-parallel programming models, such as Cilk and OpenMP raise the level of abstraction for expressing parallelism, but still require explicit synchronization. Recent task-based programming models use implicit synchronization, using a task’s memory footprint, either inferred by the system or declared by the programmer, to avoid concurrent accesses, or even achieve fully deterministic execution [1, 4].

Statically prohibiting concurrent access to shared memory among parallel tasks is too restrictive, as in many cases only a few instances of conflicting tasks will actually conflict. For this reason, even existing statically verifiable non-dependent task models relax the requirement for strictly non-overlapping task memory footprints. Instead, they use dynamic techniques to detect and correct conflicts at runtime [2].

However, dynamic dependence analysis incurs a high overhead compared to hand-crafted synchronization. It requires a complex runtime system to manage and track memory allocation, check for conflicts, and schedule parallel tasks. Often, the runtime cost of checking for conflicts in pessimistic, or rolling back a task in optimistic runtimes becomes itself a bottleneck, limiting the achievable speedup.

This work aims to alleviate the overhead of dynamic dependence analysis without sacrificing the benefit of implicit synchronization. We use a static dependence analysis to detect and remove runtime dependence checks when unnecessary. We make the following contributions:

- We develop a static analysis that detects independent task arguments and reduces runtime overhead in task-parallel programs with implicit synchronization.
- We implement our analysis in SCOOP, a compiler that extends C with parallel tasks and task-footprint annotations, similar to OpenMP and SMPSs [3]. SCOOP targets the BDDT runtime dependence analysis, improving its performance by inferring and removing unnecessary runtime checks.
- We evaluate the efficiency and precision of the analysis on a representative set of task-parallel benchmarks. SCOOP found 28 out of 31 independent task-arguments in all benchmarks, improving performance by 12% on average and by up to 40%.

2. DESIGN AND IMPLEMENTATION
We have implemented our analysis in SCOOP, a compiler for task-parallel programs with implicit synchronization. We use the BDDT runtime system to schedule tasks and perform the runtime dependence checks that cannot be statically eliminated.

SCOOP is structured as three modules. The first extends the C front-end with support for OpenMP-like #pragma directives to define tasks and SMPSS-like syntax to define task footprints. The syntax for declaring task footprints supports strided memory access patterns, so that we can describe multidimensional array tiles as task arguments. When not explicitly given, we assume that the size of a task argument is the size of its type.

The second SCOOP module uses a type-system to generate points-to and control flow constraints, and then solves them to infer argument independence. To increase the analysis precision, we use a context-sensitive, field sensitive points-to analysis, and a context-sensitive control flow analysis.
Moreover, in several benchmarks tasks within loops access disjoint parts of the same array. However, the points-to analysis treats all array elements as one abstract location, producing false aliasing and causing such safe arguments to be missed. To rectify this, in part, we have implemented a simple loop-dependence analysis that discovers when different loop iterations access non-overlapping array elements. This (orthogonal) problem has been extensively studied in the past, resulting in many techniques that can be applied to improve the precision of this optimization.

The final SCOOP phase transforms the input program to use BDDT for creating tasks, disabling BDDT’s runtime dependence checks for inferred or declared independent arguments. During code generation, we optimize the output code by producing custom code to interact with the dependence analysis and scheduler, instead of using generic BDDT API calls. This way, SCOOP is able to optimize away loops, va_args, some conditionals, and treat scalar arguments specially (by value), further reducing the runtime overhead of creating a task, compared to a generic API for task creation.

BDDT uses a region-based allocator to support dynamic memory allocation in tasks, and allow for tasks that operate on complex data structures. Hence, SCOOP handles task footprints that include dynamic regions specially. Specifically, the task footprint language allows several task arguments to belong to a dynamic region; the task footprint then includes the region instead of the individual arguments, and SCOOP registers only the region descriptor with the dependence analysis.

## 3. EVALUATION

We evaluate SCOOP on 10 benchmarks and measure the precision and performance of the analysis, as well as the impact of the resulting optimization on execution time and scalability. The left part of Table 1 shows the characterization of each benchmark. The first columns show the size of the source code processed by SCOOP, the number of static task definitions, the number of the total arguments used by the tasks, and the number of scalar arguments, which are not processed by the static analysis, respectively.

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>LOC</th>
<th>Tasks</th>
<th>Total Args</th>
<th>Scalar Args</th>
<th>Analysis Time (s)</th>
<th>Graph Nodes</th>
<th>Safe Args</th>
<th>BDDT (ms)</th>
<th>SCOOP (ms)</th>
<th>Speedup</th>
</tr>
</thead>
<tbody>
<tr>
<td>Black-Scholes</td>
<td>3564</td>
<td>1</td>
<td>8</td>
<td>1</td>
<td>3.17</td>
<td>1790</td>
<td>7</td>
<td>1618</td>
<td>963</td>
<td>1.68</td>
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<td>Ferret</td>
<td>30145</td>
<td>1</td>
<td>2</td>
<td>0</td>
<td>699.05</td>
<td>85128</td>
<td>2</td>
<td>3344</td>
<td>3344</td>
<td>1.00</td>
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<tr>
<td>Cholesky</td>
<td>1734</td>
<td>4</td>
<td>16</td>
<td>8</td>
<td>1.06</td>
<td>7571</td>
<td>0</td>
<td>983</td>
<td>981</td>
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<tr>
<td>GMRES</td>
<td>2661</td>
<td>18</td>
<td>72</td>
<td>20</td>
<td>2.21</td>
<td>7957</td>
<td>9</td>
<td>16640</td>
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<tr>
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<td>11</td>
<td>63</td>
<td>35</td>
<td>1.47</td>
<td>9330</td>
<td>0</td>
<td>1628</td>
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<td>0</td>
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<td>3980</td>
<td>0</td>
<td>11499</td>
<td>11588</td>
<td>0.99</td>
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<tr>
<td>SMPSs-FFT</td>
<td>2278</td>
<td>8</td>
<td>36</td>
<td>25</td>
<td>1.17</td>
<td>9270</td>
<td>0</td>
<td>1077</td>
<td>1042</td>
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<tr>
<td>SPLASH-FFT</td>
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<td>4</td>
<td>12</td>
<td>4</td>
<td>1.72</td>
<td>9750</td>
<td>7</td>
<td>2028</td>
<td>1849</td>
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<td>0</td>
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<td>3</td>
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</table>

Table 1: Benchmark description, analysis performance, and impact on execution

Overall, the average performance improvement from applying SCOOP on all benchmarks is 12%, although this value is not representative, as actual impact greatly varies among benchmarks; the dependence analysis is able to infer safe task arguments only in Black-Scholes, Ferret, GMRES, SPLASH-FFT and Intruder. In these benchmarks, inferring independent arguments has a large impact on the overhead and scalability of the dependence analysis, producing substantial speedup over the original BDDT versions for four benchmarks. The reduction of dependence analysis overhead is not noticeable in Ferret because the tasks are very coarse grain. On the other hand, the analysis did not discover any safe task arguments in the other five benchmarks, which do not gain any significant benefit from SCOOP, other than that resulting from custom code generation.

## 4. REFERENCES


